

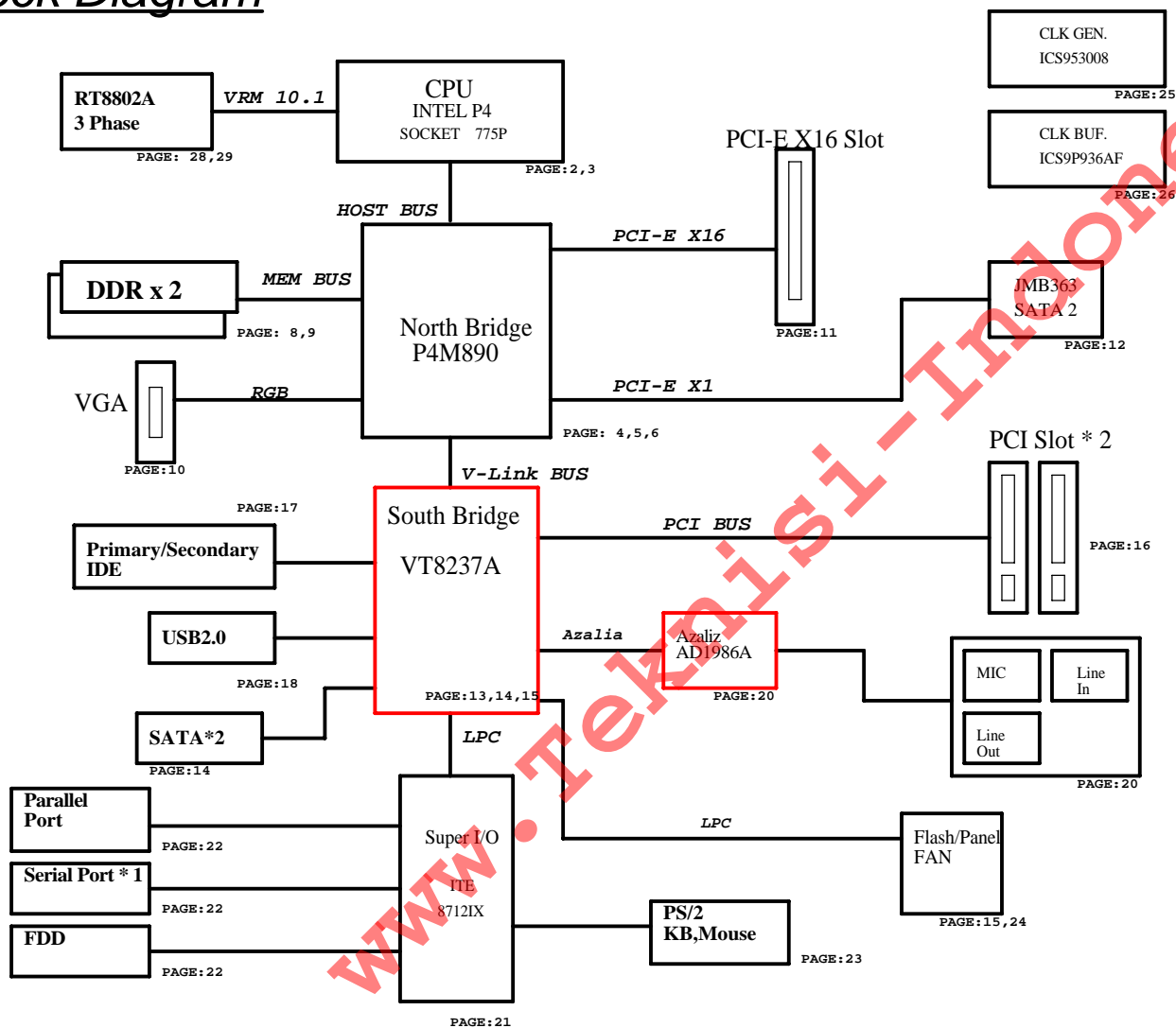
Revision : 1.03G

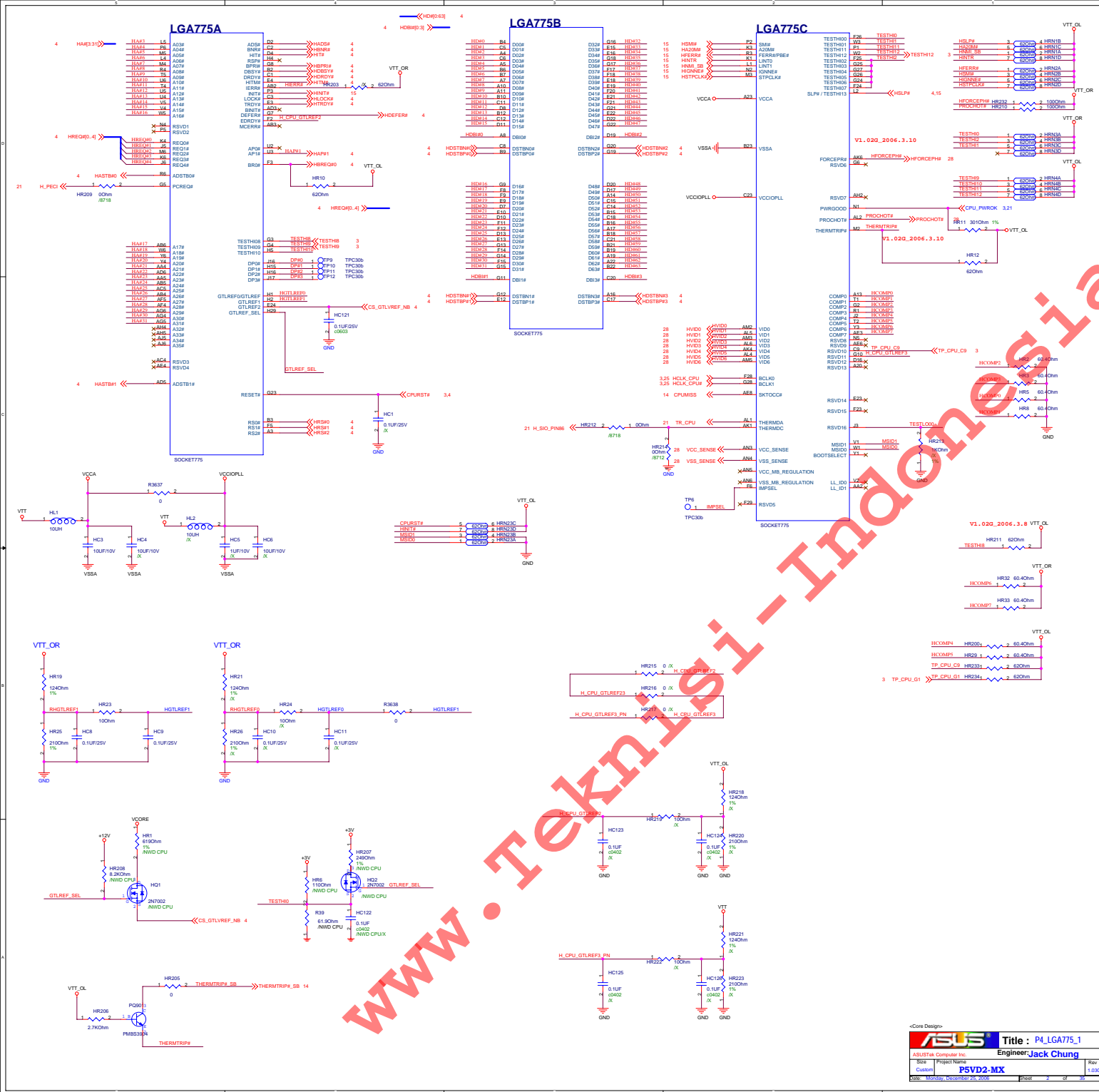


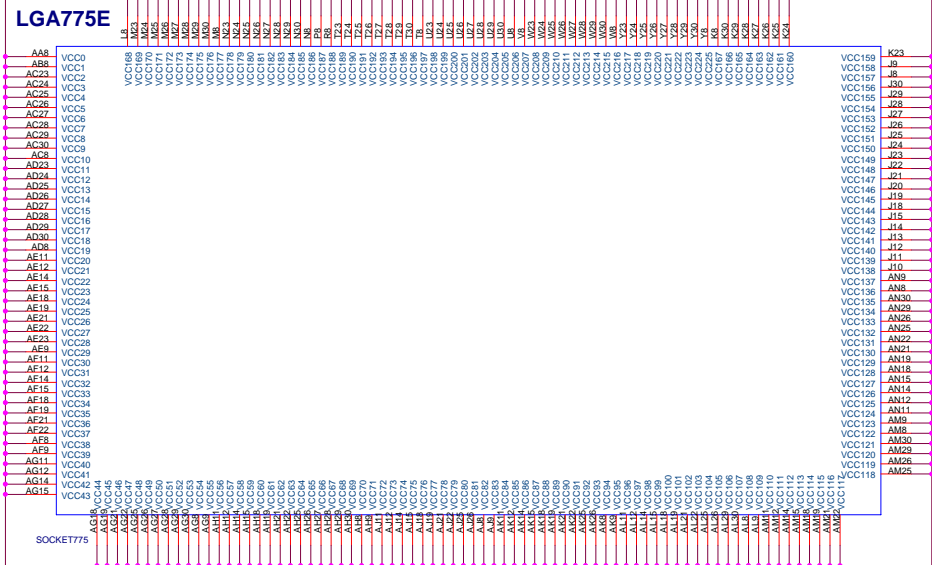
Intel LGA775 Processor
VIA P4M890 + 8237A

uATX 9.6" x 8.6"

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1	Block Diagram
2	P4_LGA775_1
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4	NB P4M890 HOST
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32	VCCM/ +2.1V_QSW/ +2.5VSB
33	GPIO Table
34	EMI/ Screwwhole/ Manual Parts
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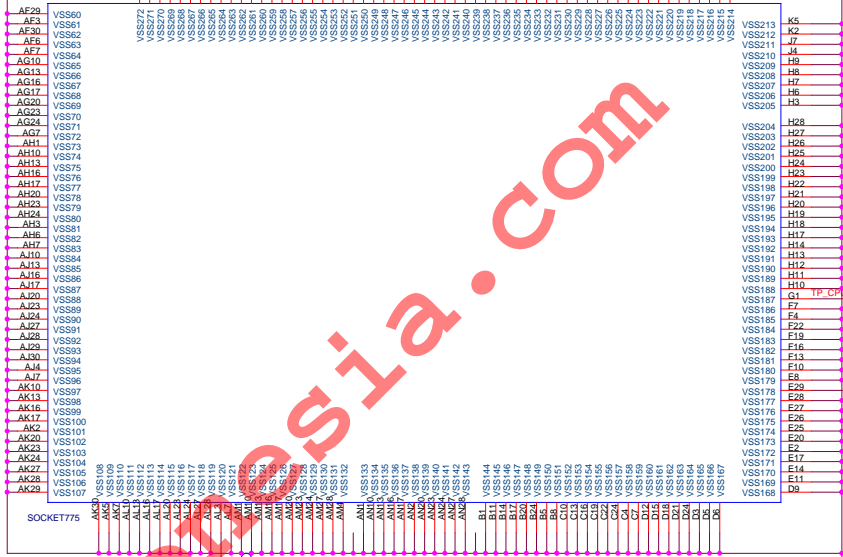


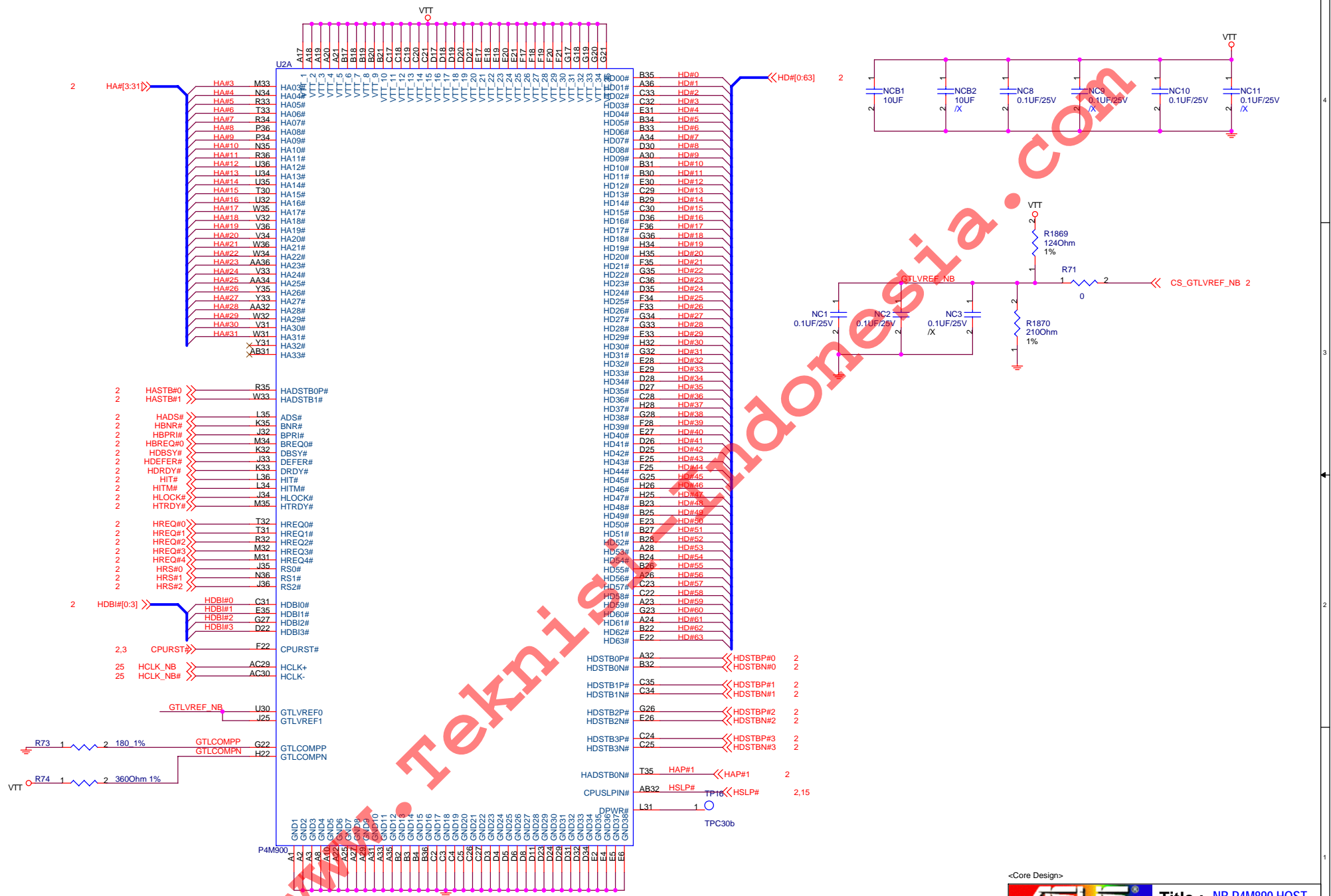




VCORE

LGA775G





<Core Design>

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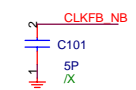
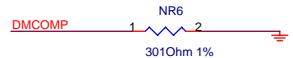
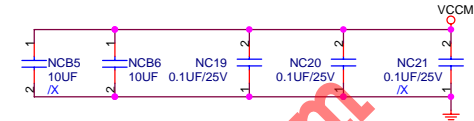
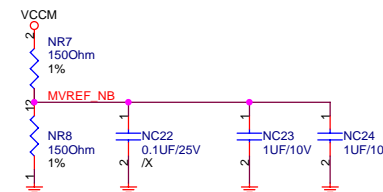
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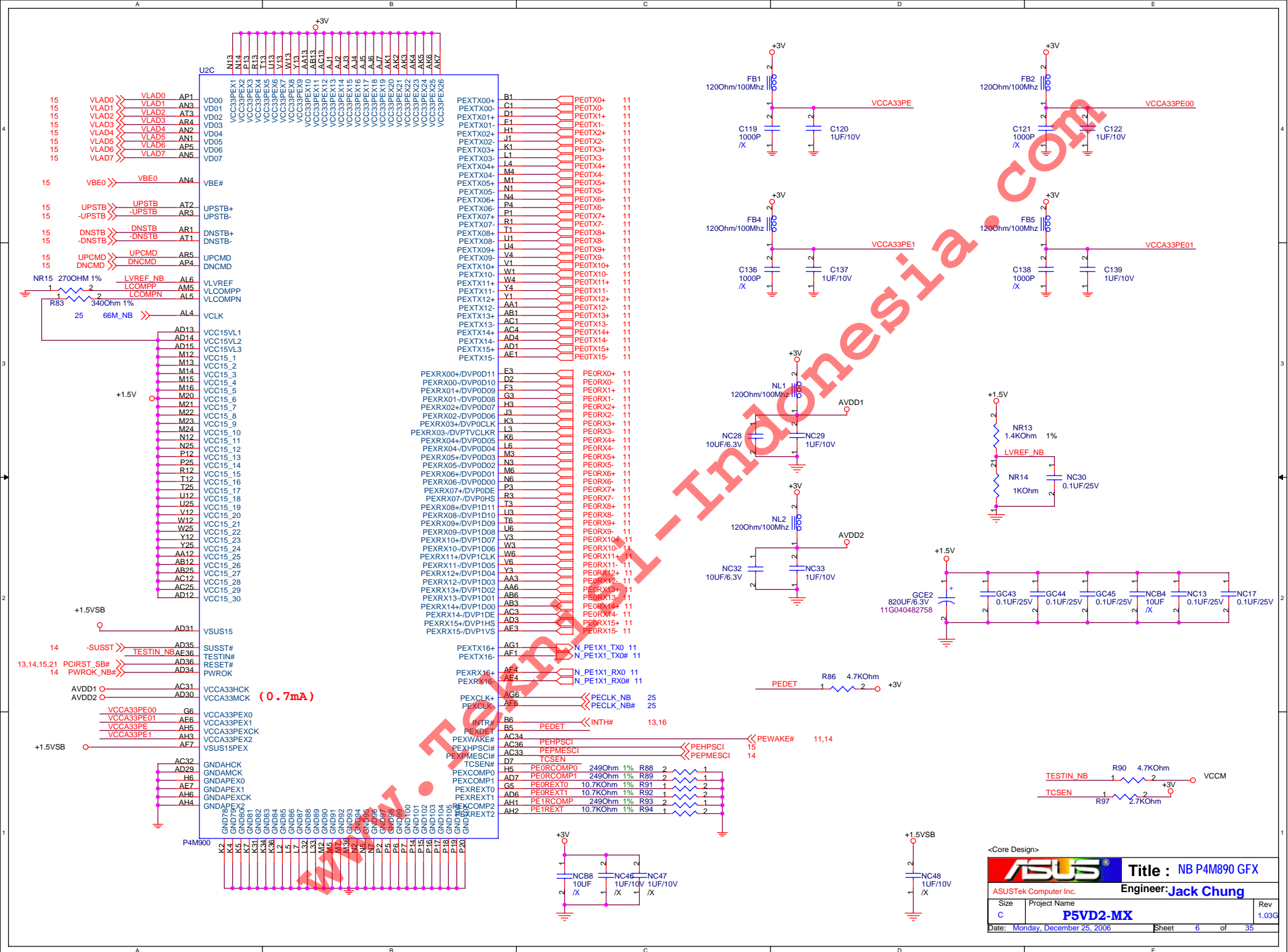
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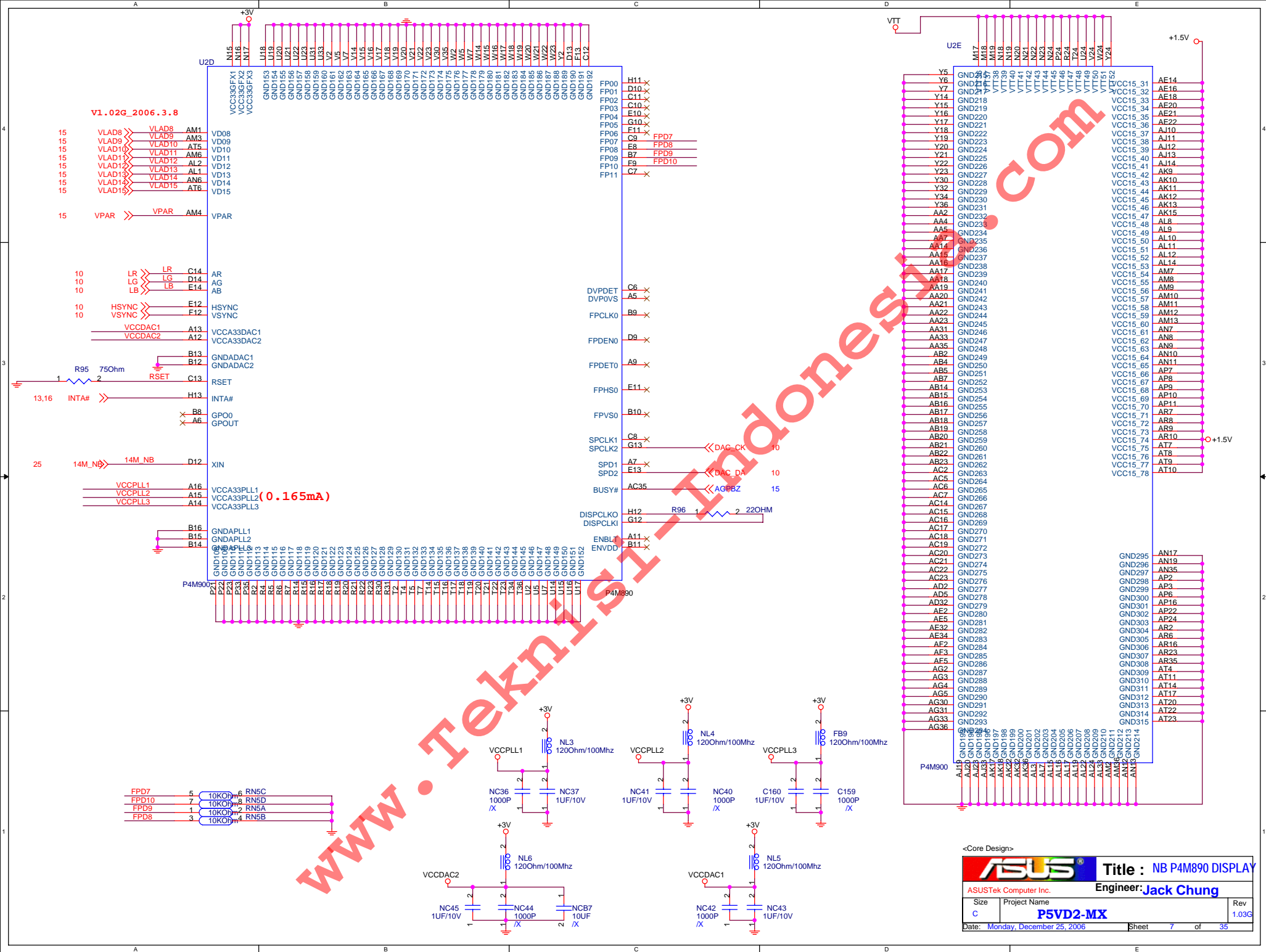
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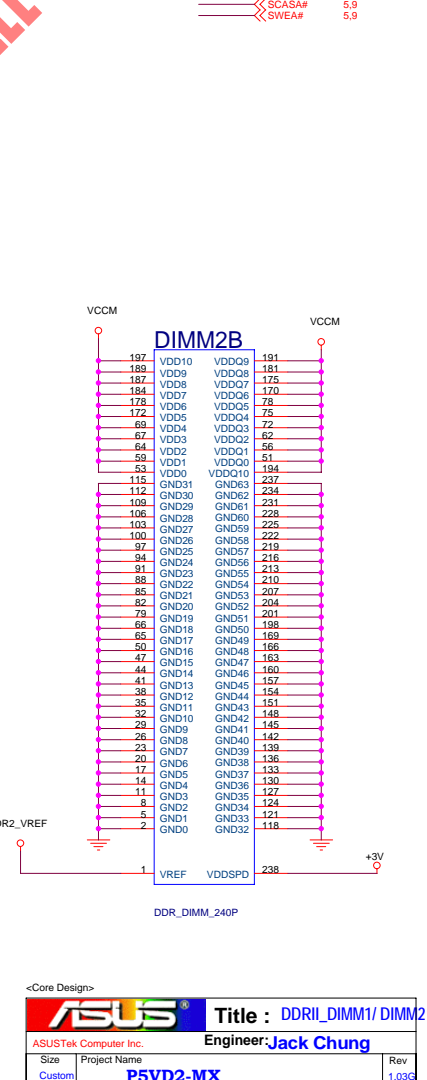
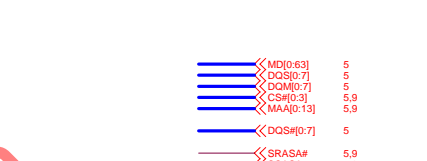
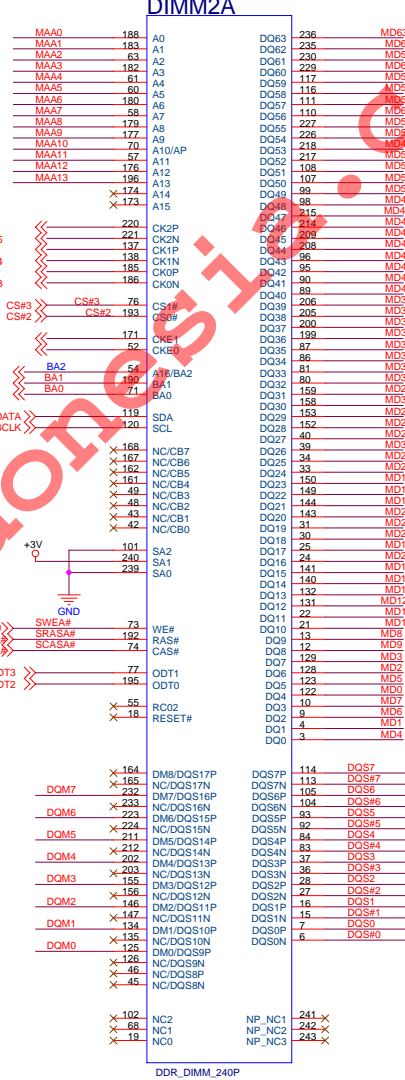
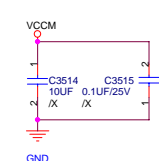
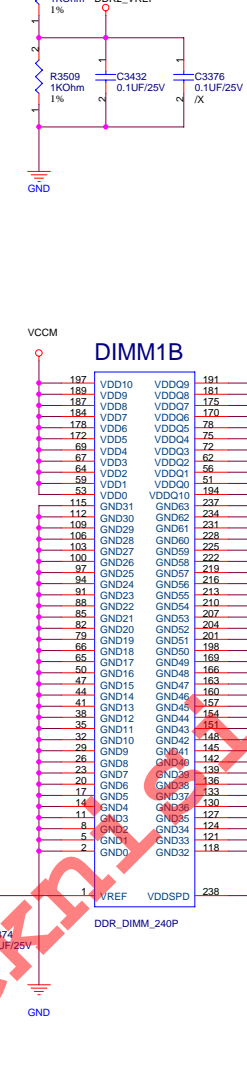
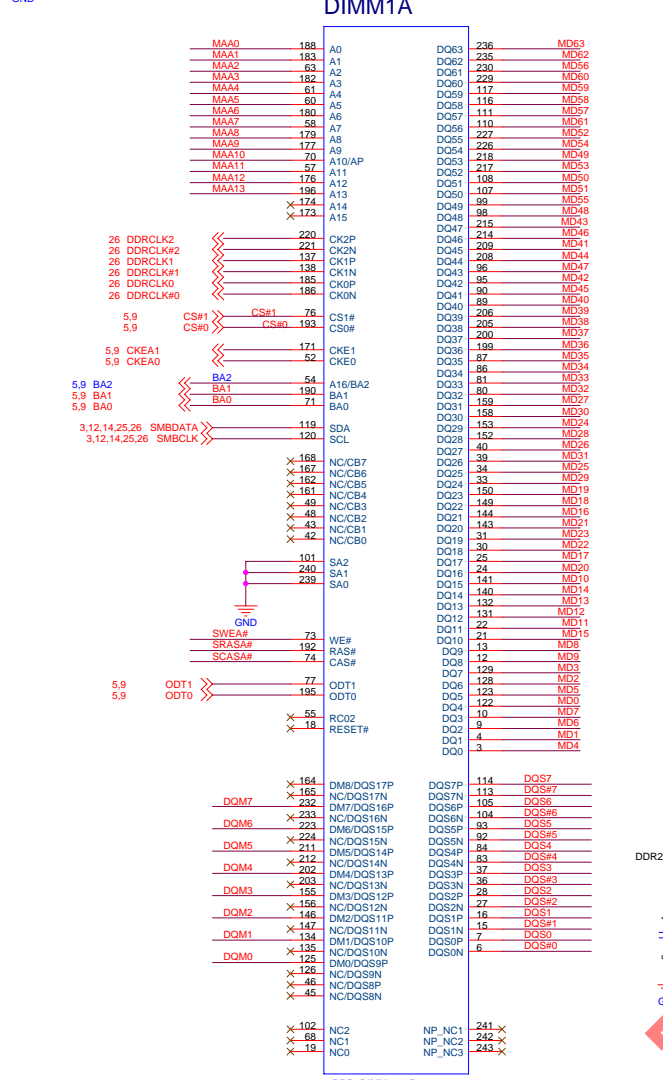
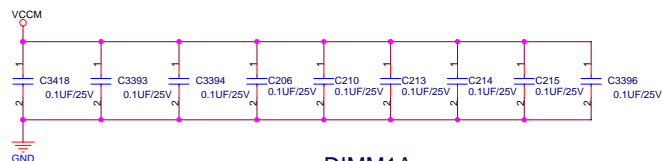
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<Core Design>

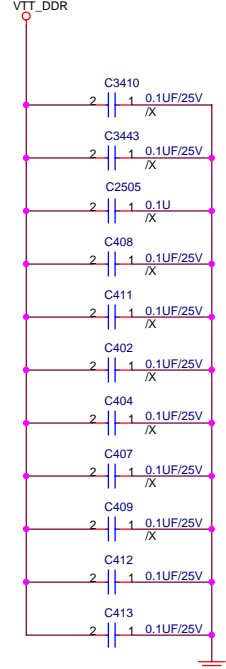
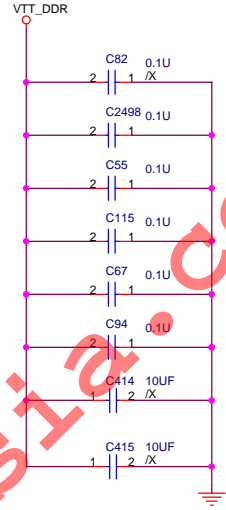
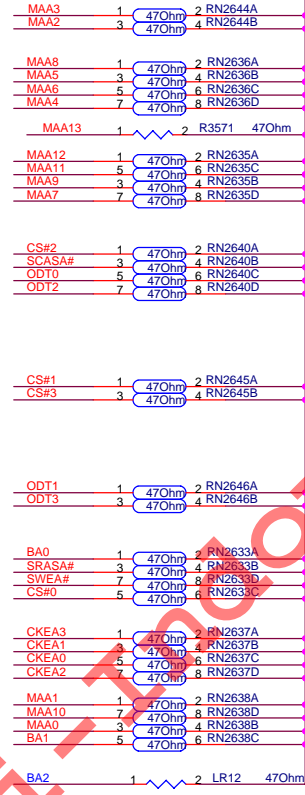
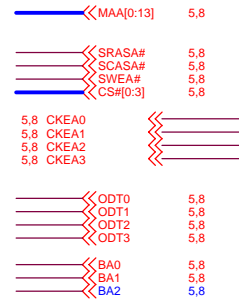
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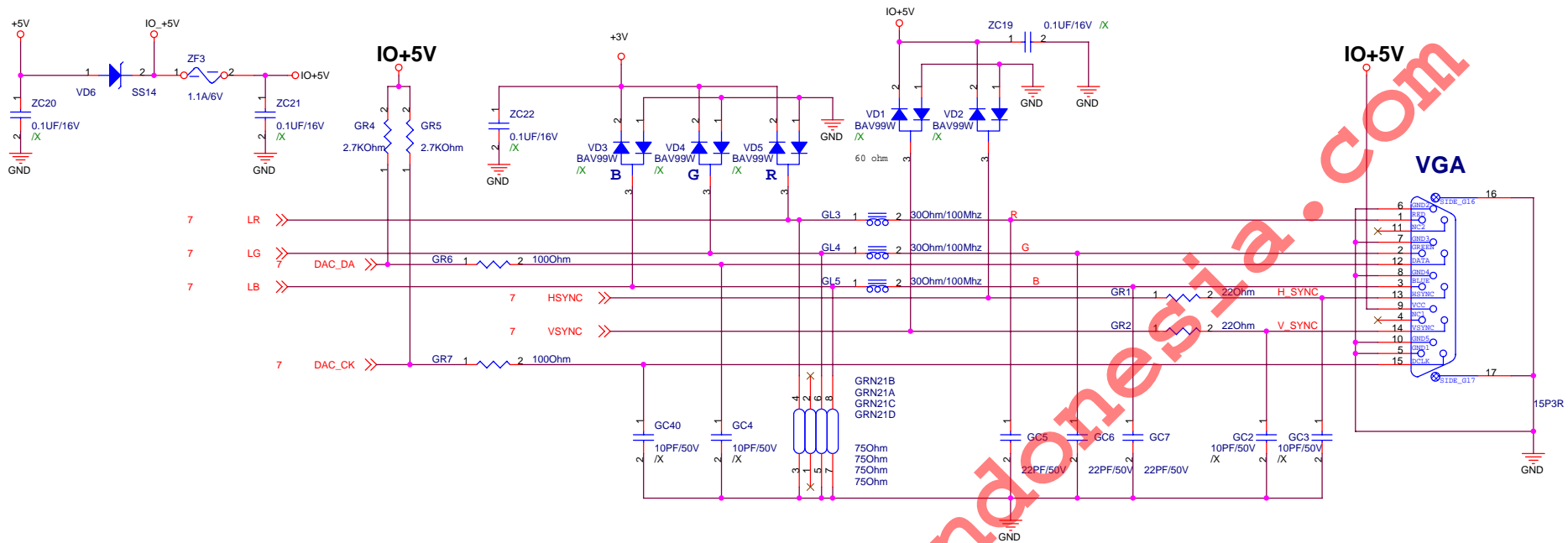
ASUSTek Computer Inc. Engineer: Jack Chung

Size	Project Name	Rev
Custom	PSVD2-MX	1.03C

Date: Monday, December 25, 2006 Sheet 8 of 35

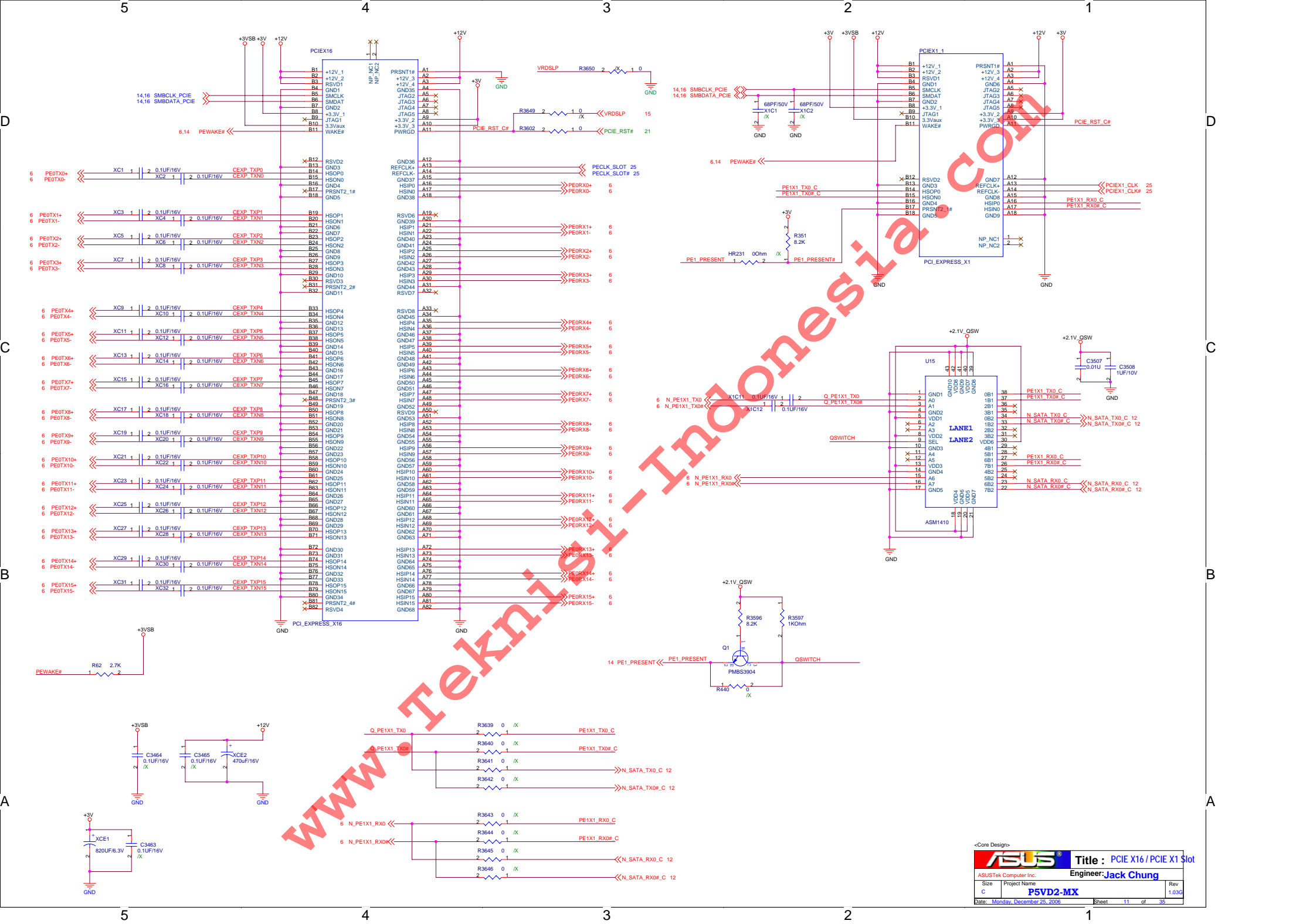
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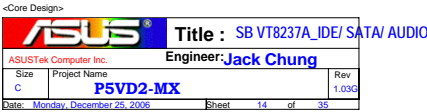




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ASUSTek Computer Inc.		Engineer: Jack Chung	
Size	Project Name	Rev	
Custom	P5VD2-MX	1.03G	
Date: Monday, December 25, 2006	Sheet 10 of 35		

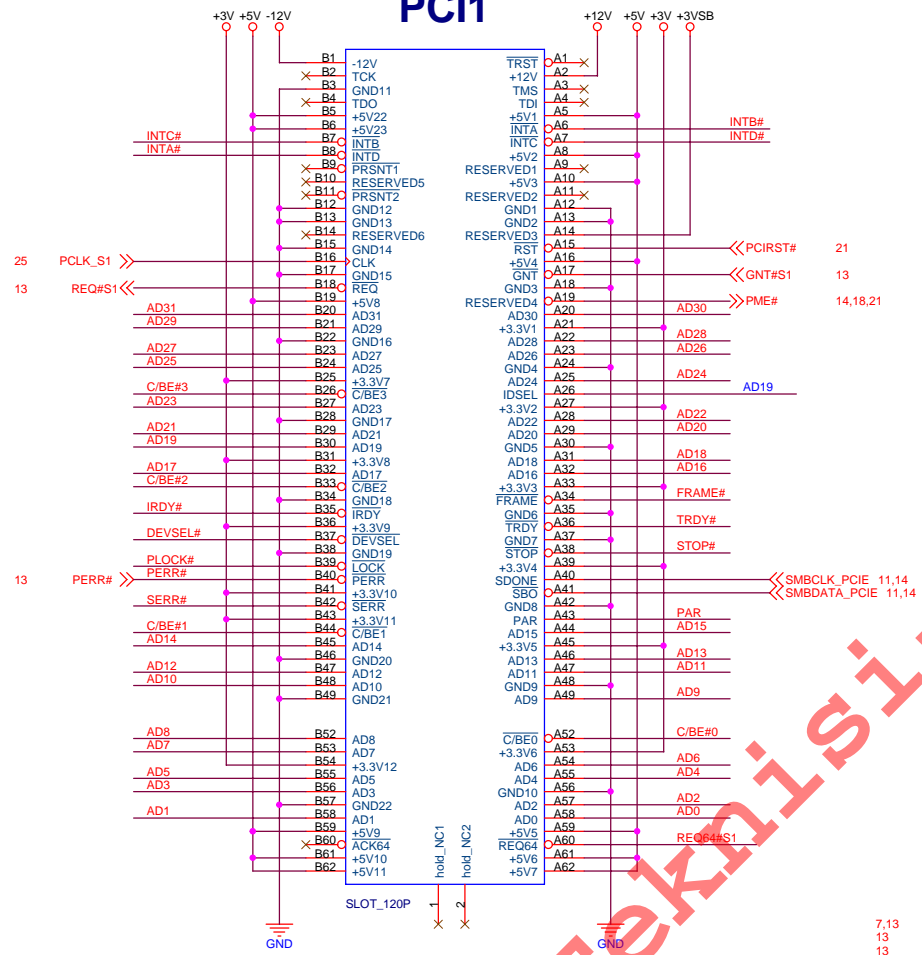




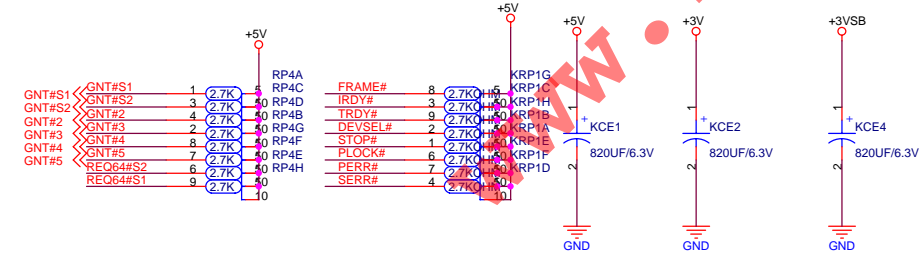
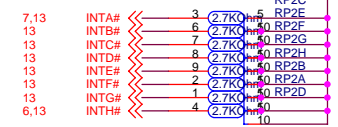
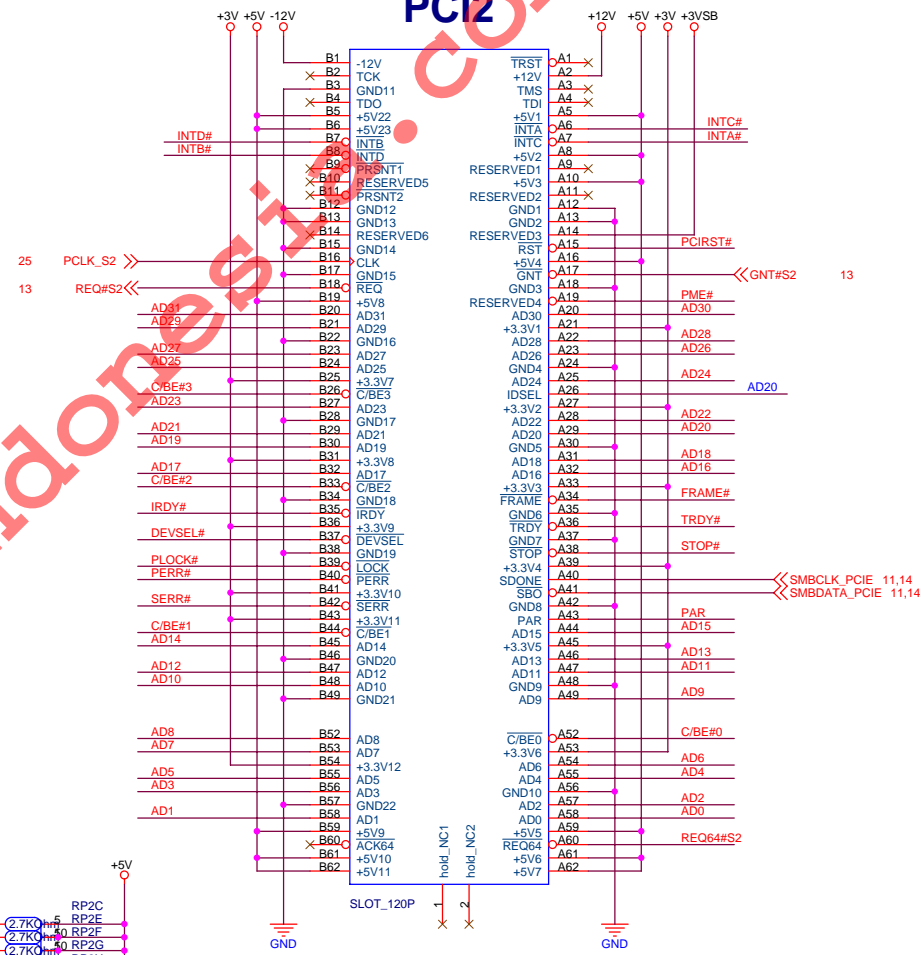
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7,13 INTA#
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13 INTC#
13 INTD#

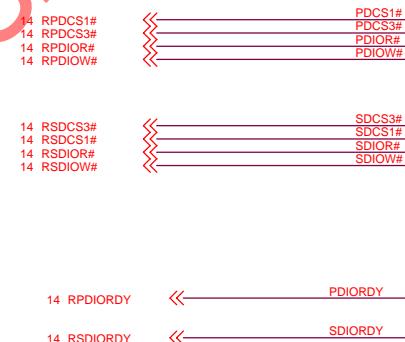
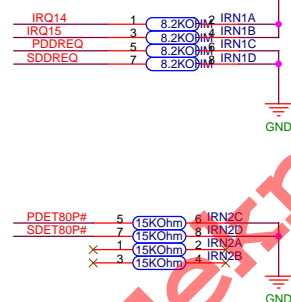
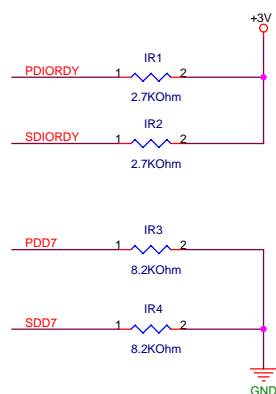
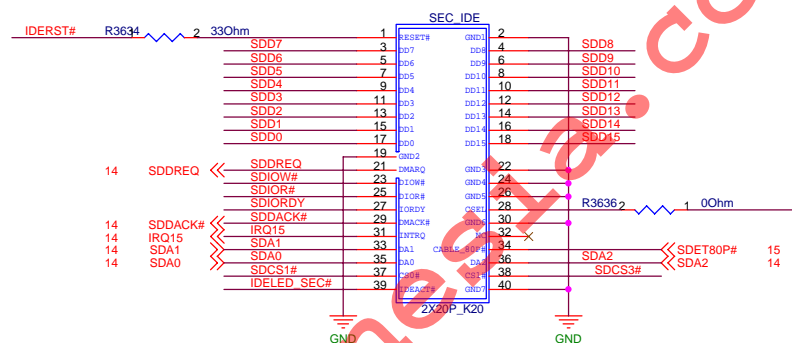
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PCI1

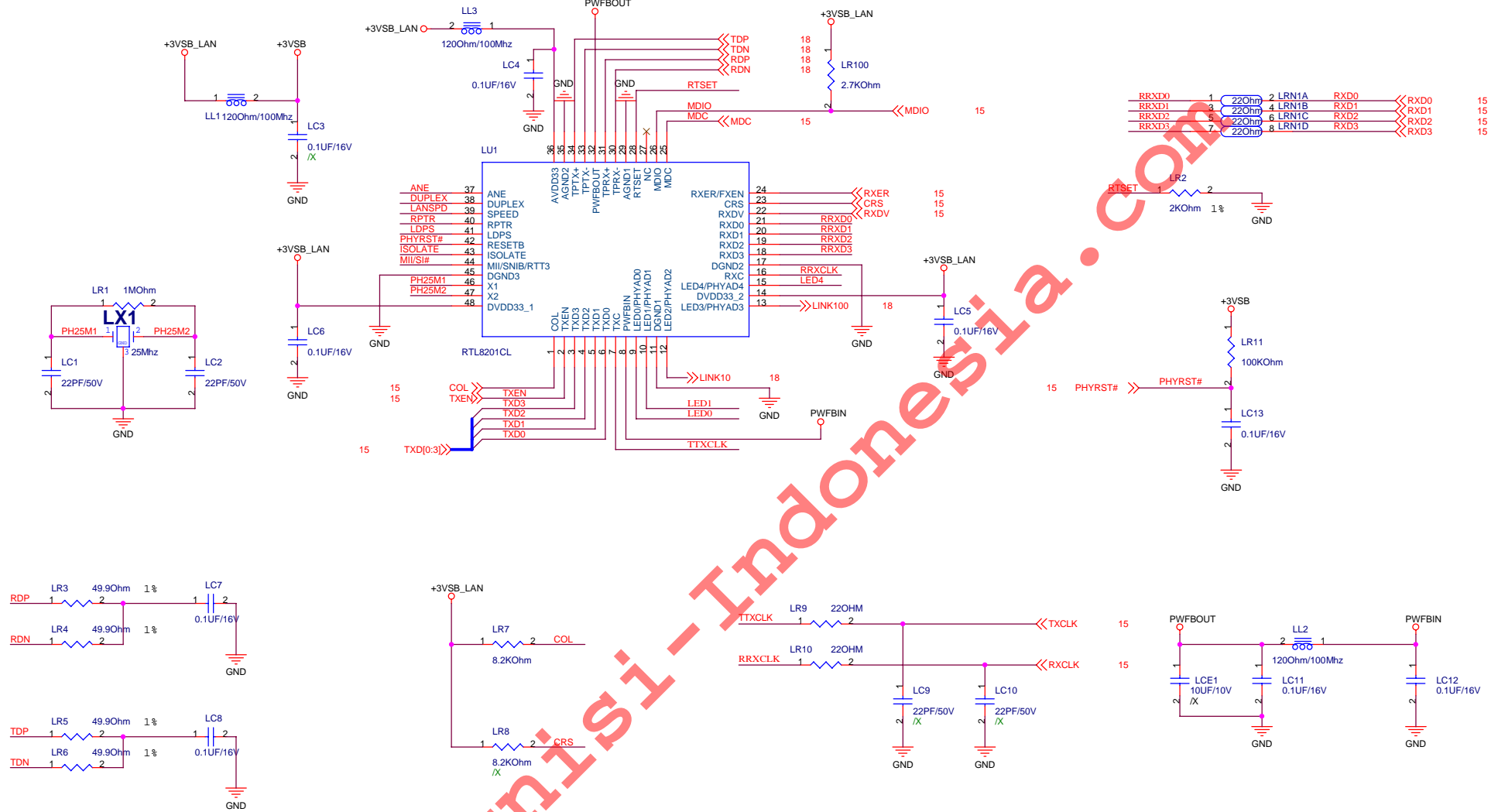


PCI2

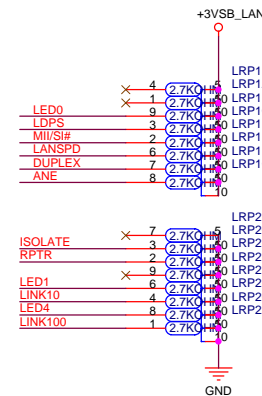




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	PDD3		RPDD3
	PDD14		RPDD14
	PDD1		RPDD1
	PDD15		RPDD15
	PDD0		RPDD0
	SDD5		RSDD5
	SDD3		RSDD3
	SDD6		RSDD6
	SDD4		RSDD4
	SDD7		RSDD7
	SDD8		RSDD8
	SDD9		RSDD9
	SDD10		RSDD10
	SDD11		RSDD11
	SDD12		RSDD12
	SDD15		RSDD15
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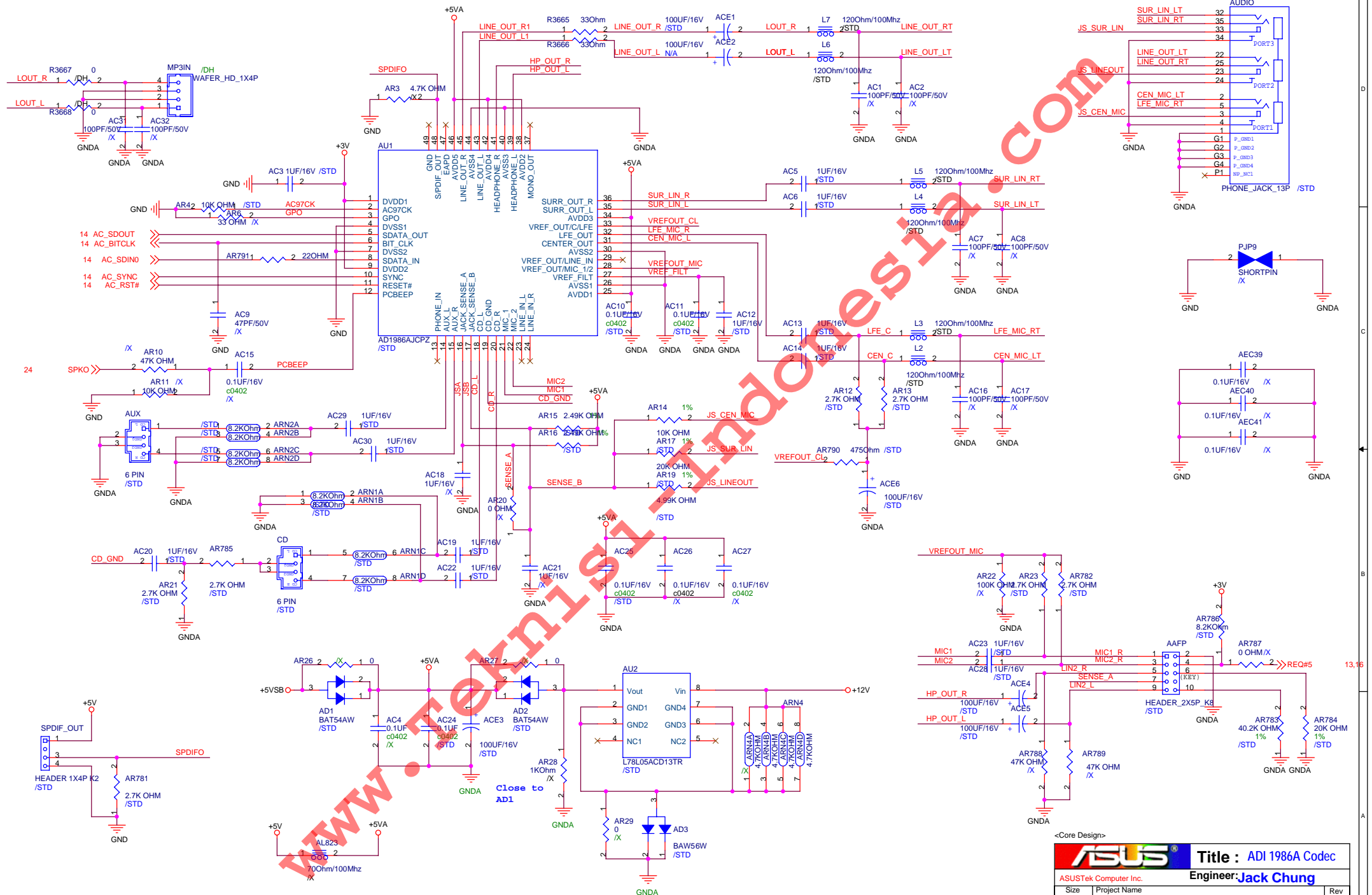


Check LED

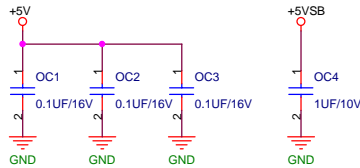
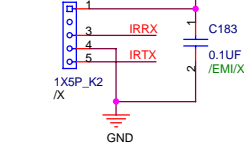


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ASUS		Title : RTL8201CL 10/100 LAN	
ASUSTek Computer Inc.		Engineer: Jack Chung	
Size	Project Name	Rev	
A3	P5VD2-MX	1.03G	
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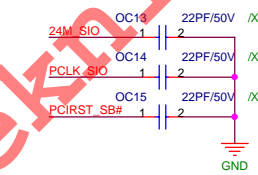
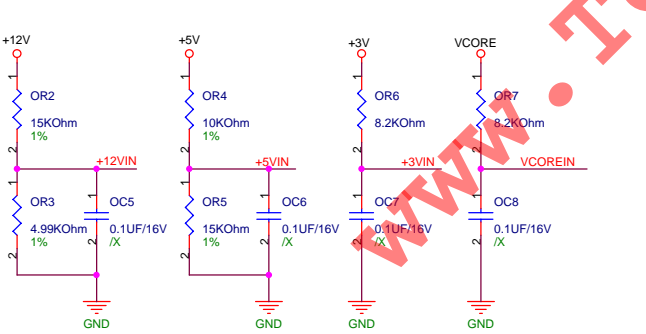
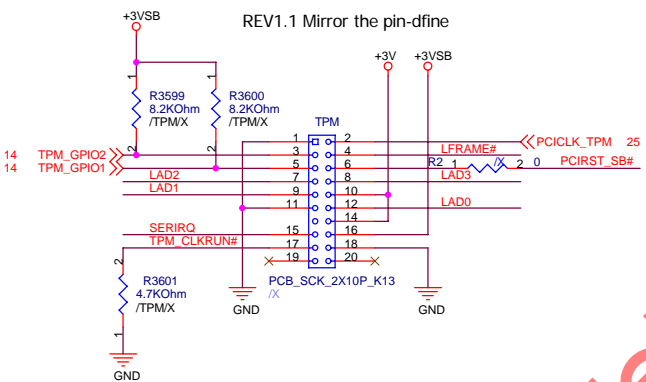
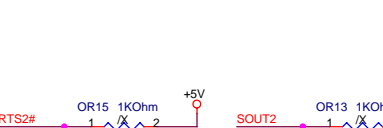
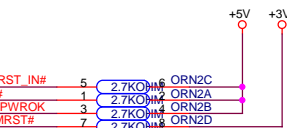
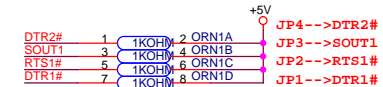
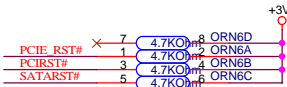
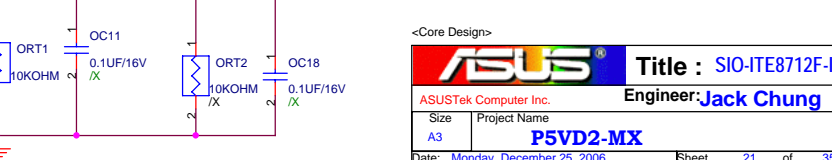
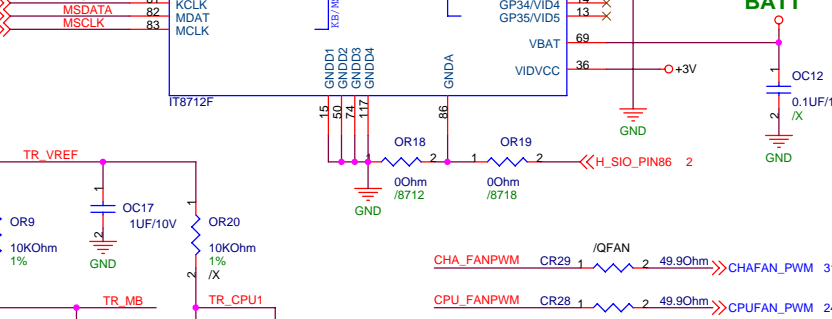
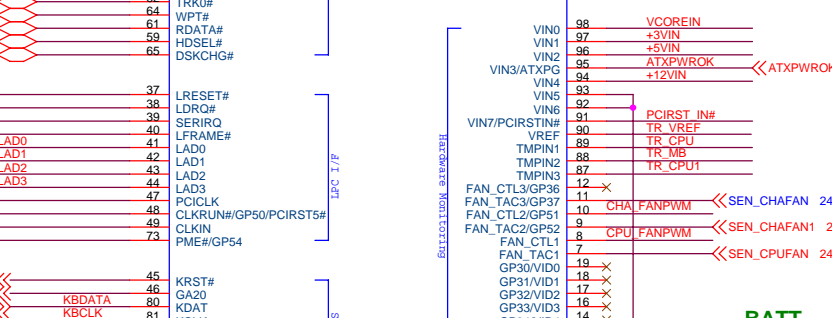
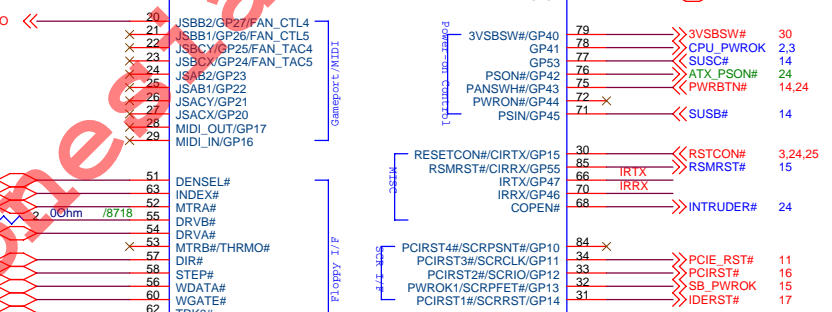
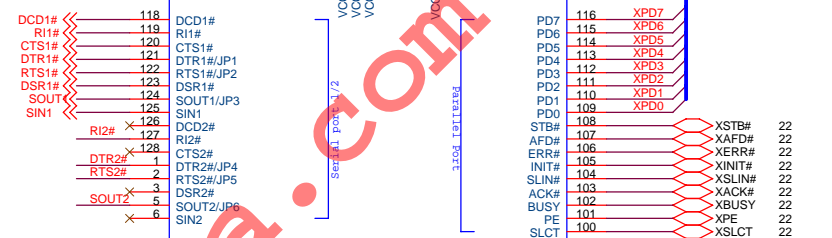


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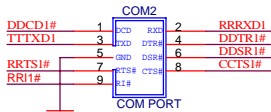
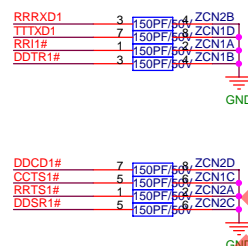
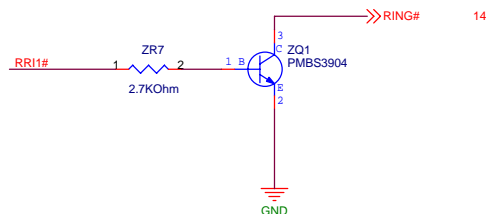
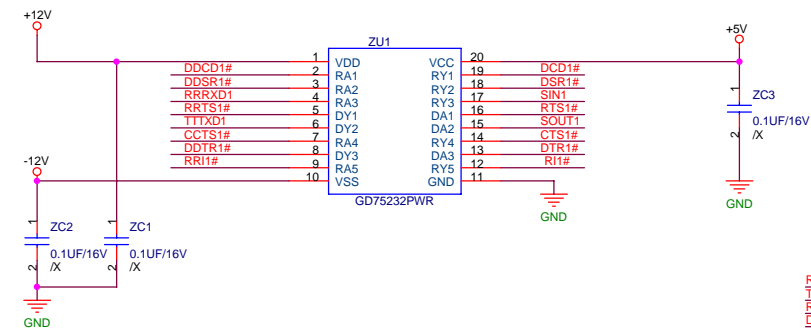
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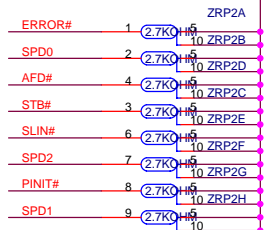
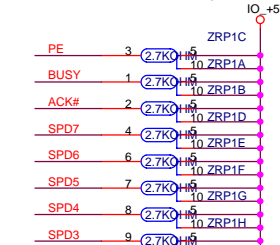
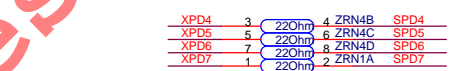
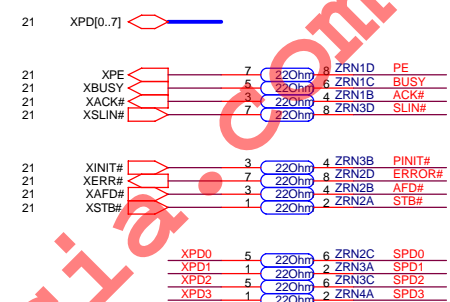
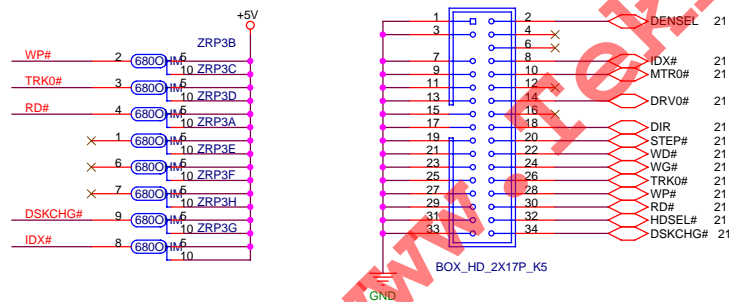
ASUSTek Computer Inc. Engineer: Jack Chung

Size	Project Name	Rev
A3	P5VD2-MX	1.03G

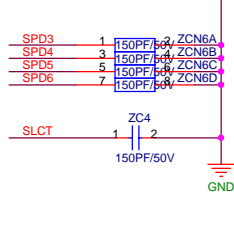
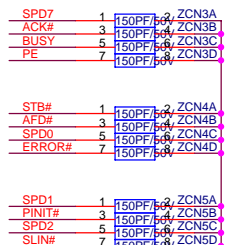
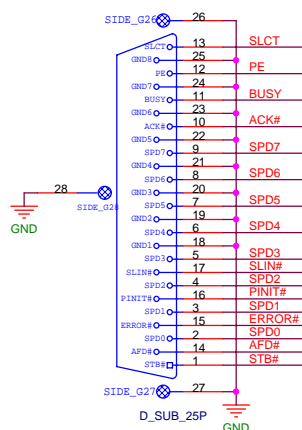
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FLOPPY

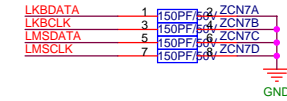
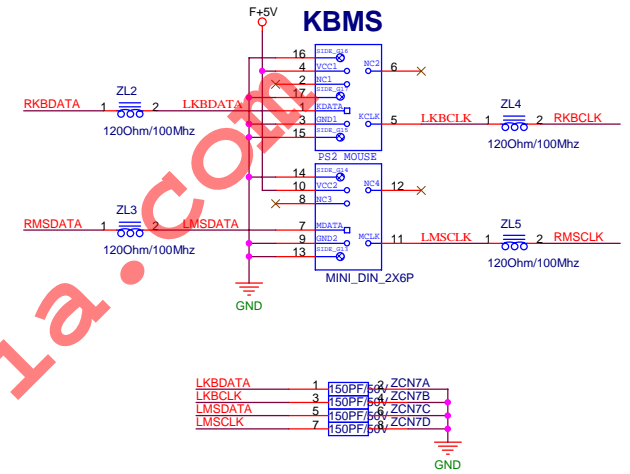
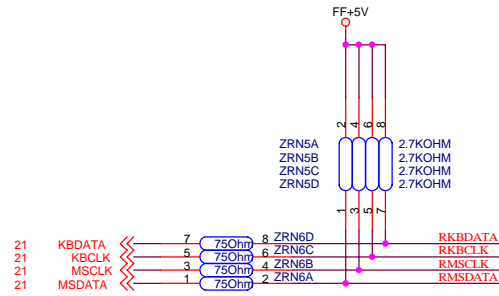
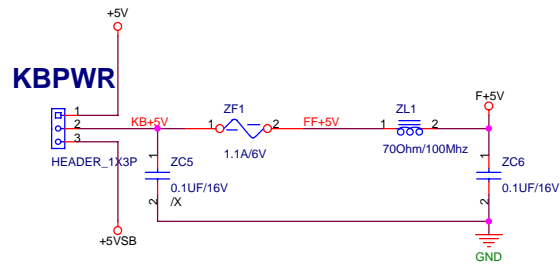


LPT



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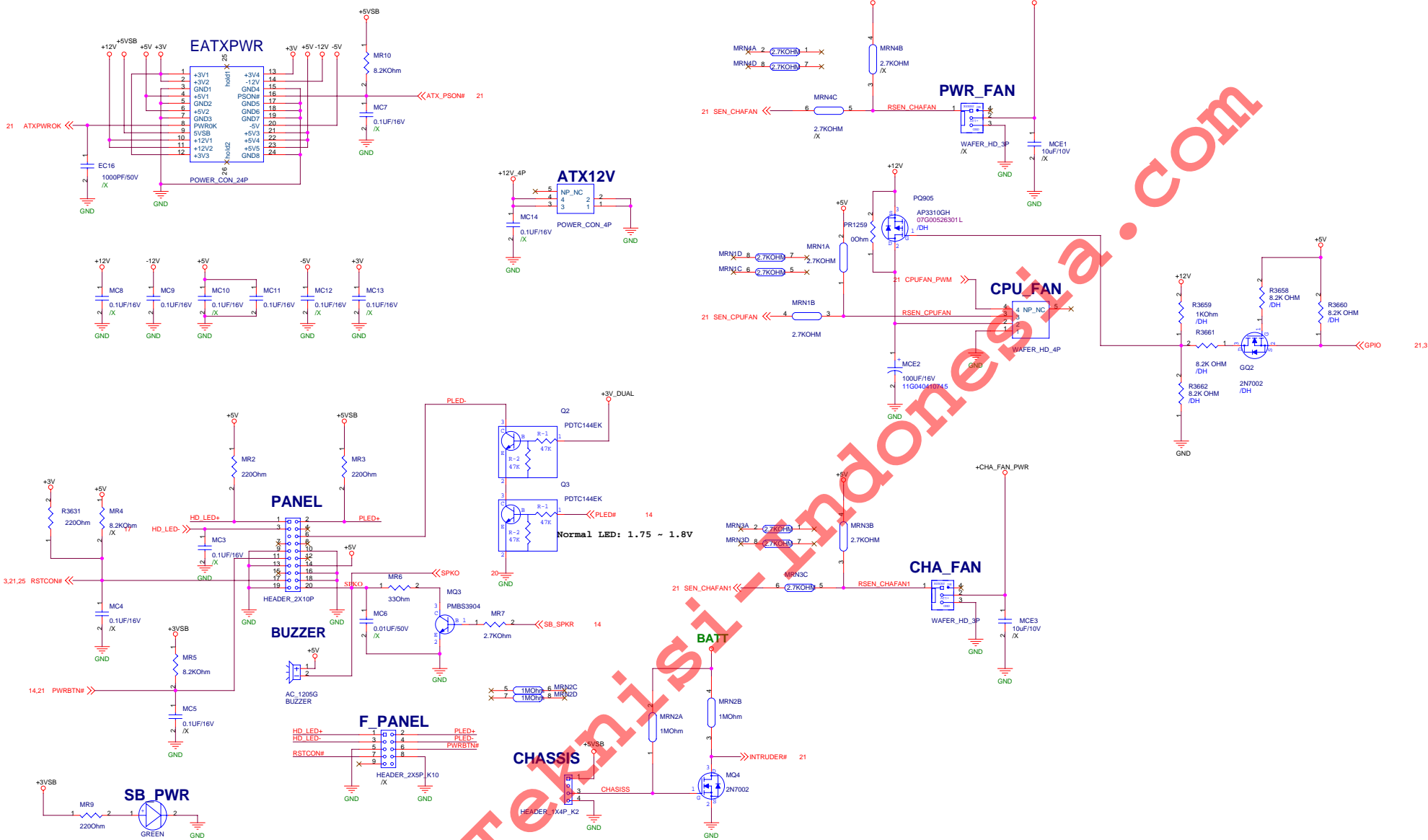
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ASUSTek Computer Inc.		Engineer: Jack Chung	
Size A3	Project Name P5VD2-MX	Rev 1.03G	
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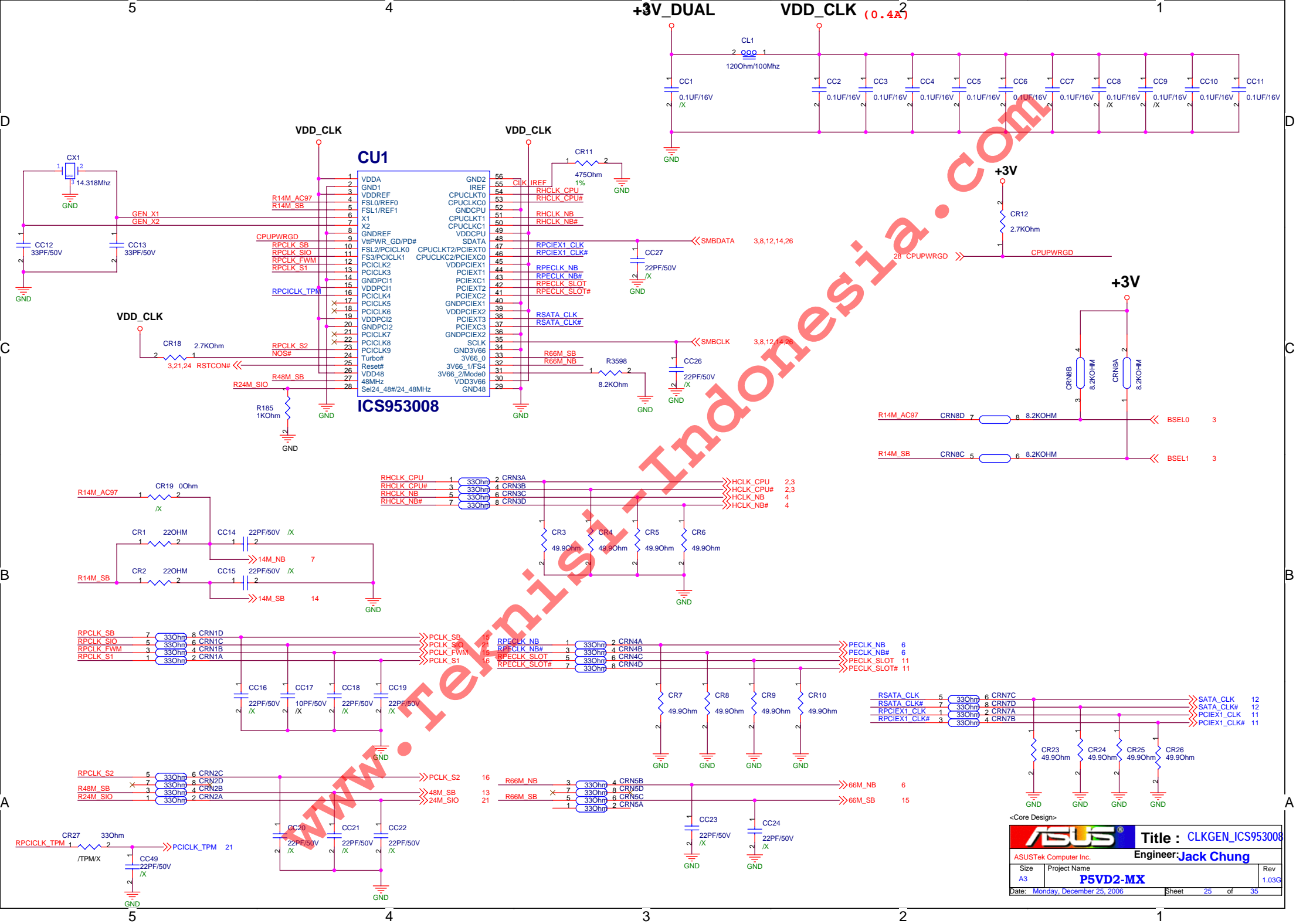
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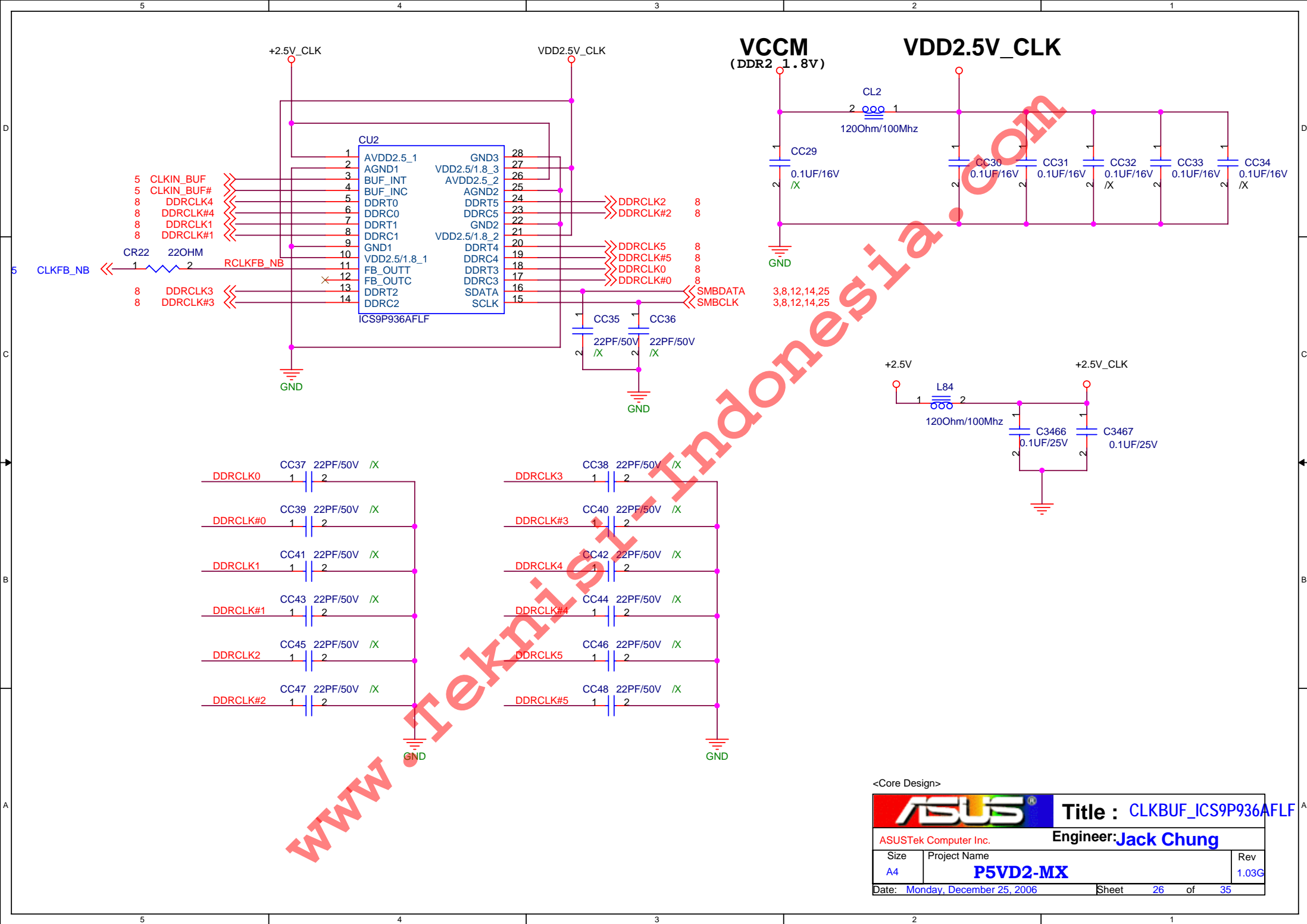
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Size	Project Name	Rev	
A3	P5VD2-MX	1.03G	
Date: Monday, December 25, 2006		Sheet	23 of 35



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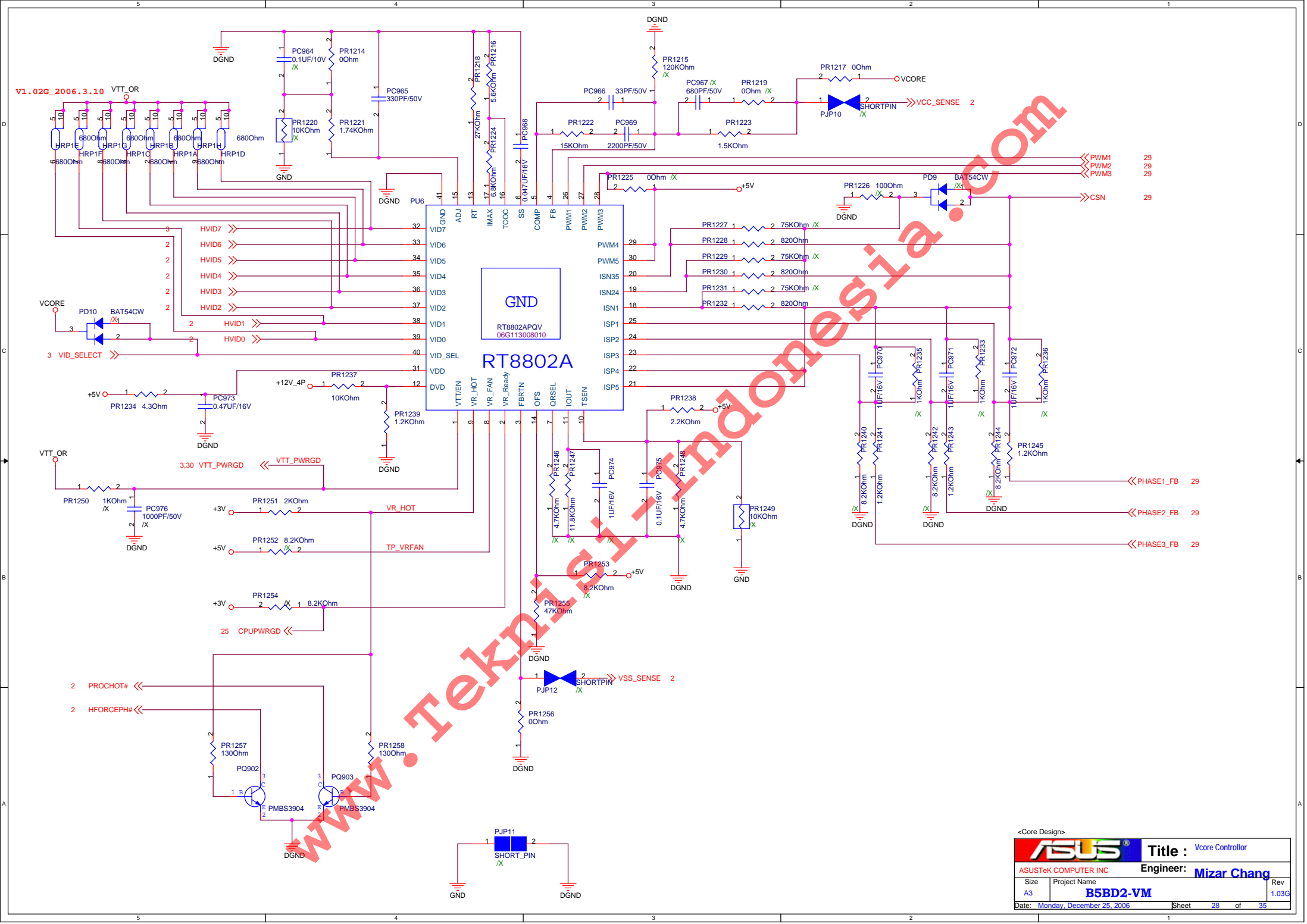
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ASUSTek Computer Inc.		Engineer: Jack Chung	
Size	Project Name	Rev	
C	P5VD2-MX	1.03G	
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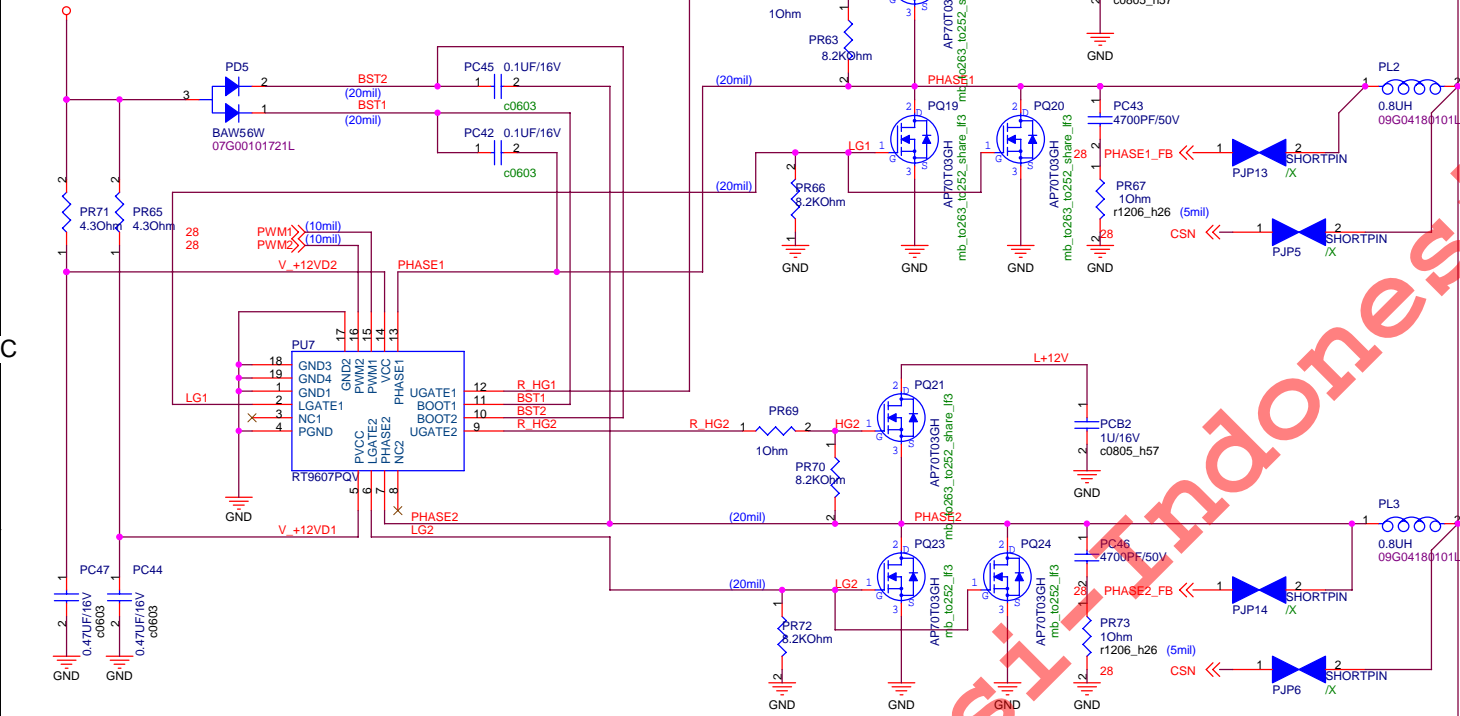


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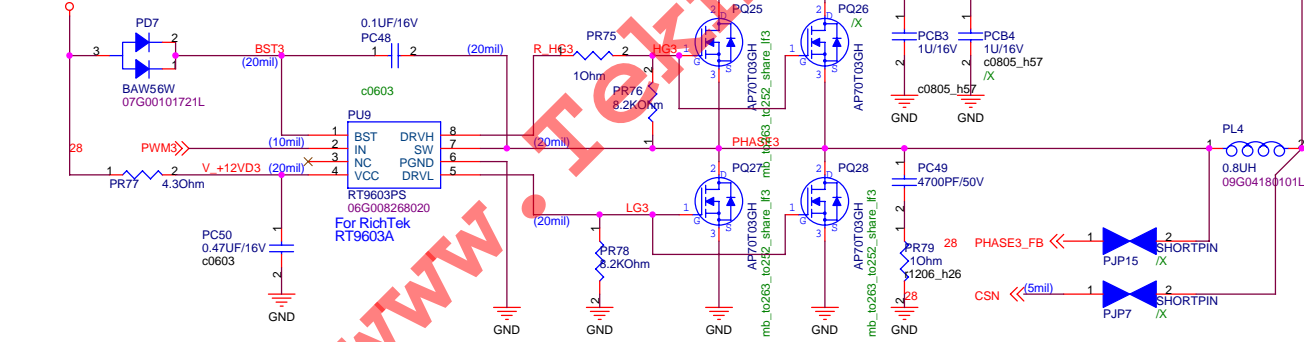
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ASUSTek Computer Inc.		Engineer: Jack Chung	
Size	Project Name	Rev	
A4	P5VD2-MX	1.03G	
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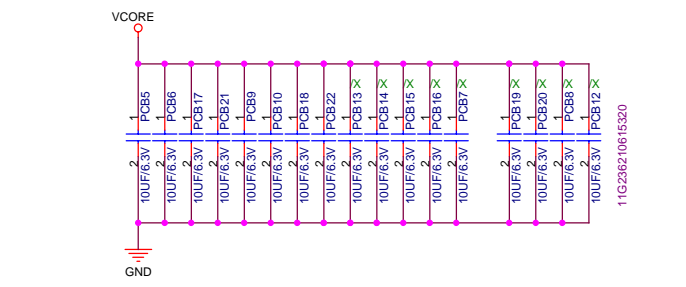
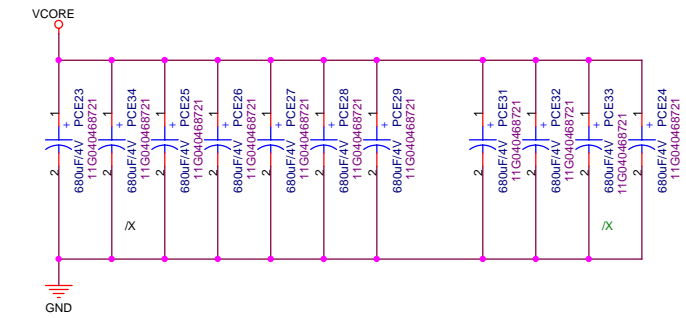
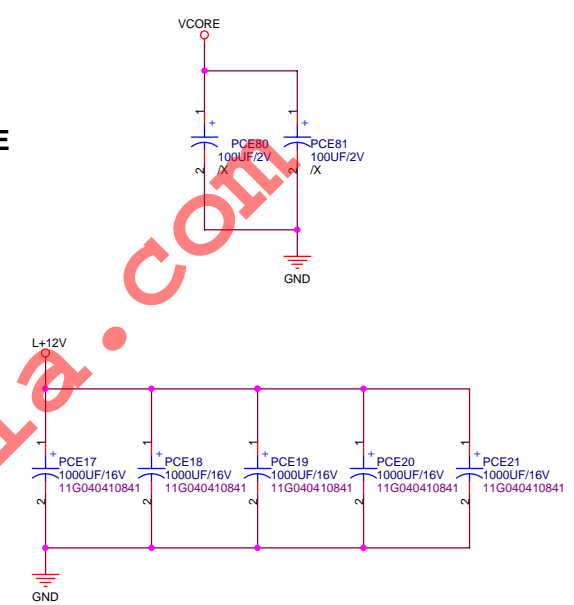
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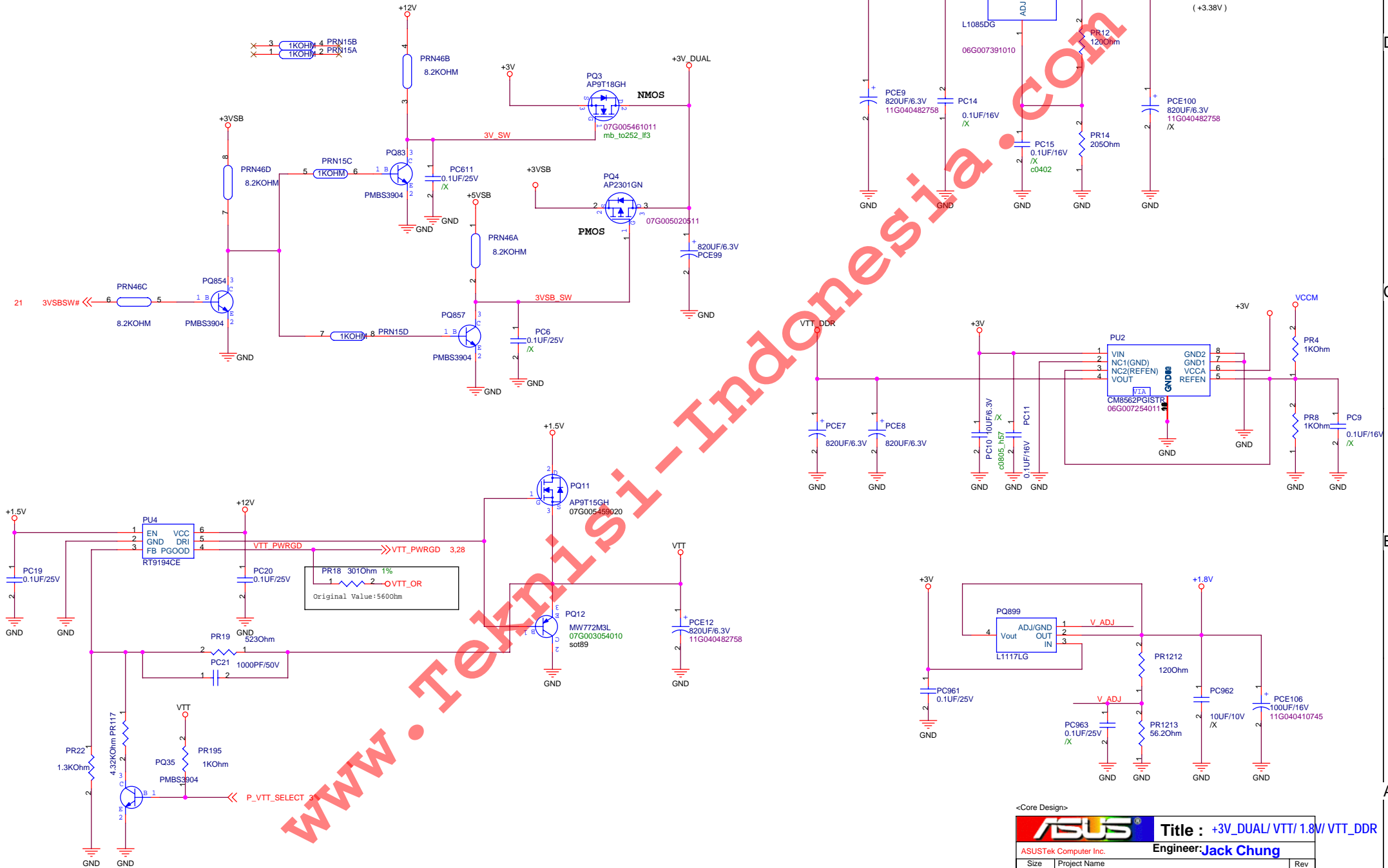
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VCORE

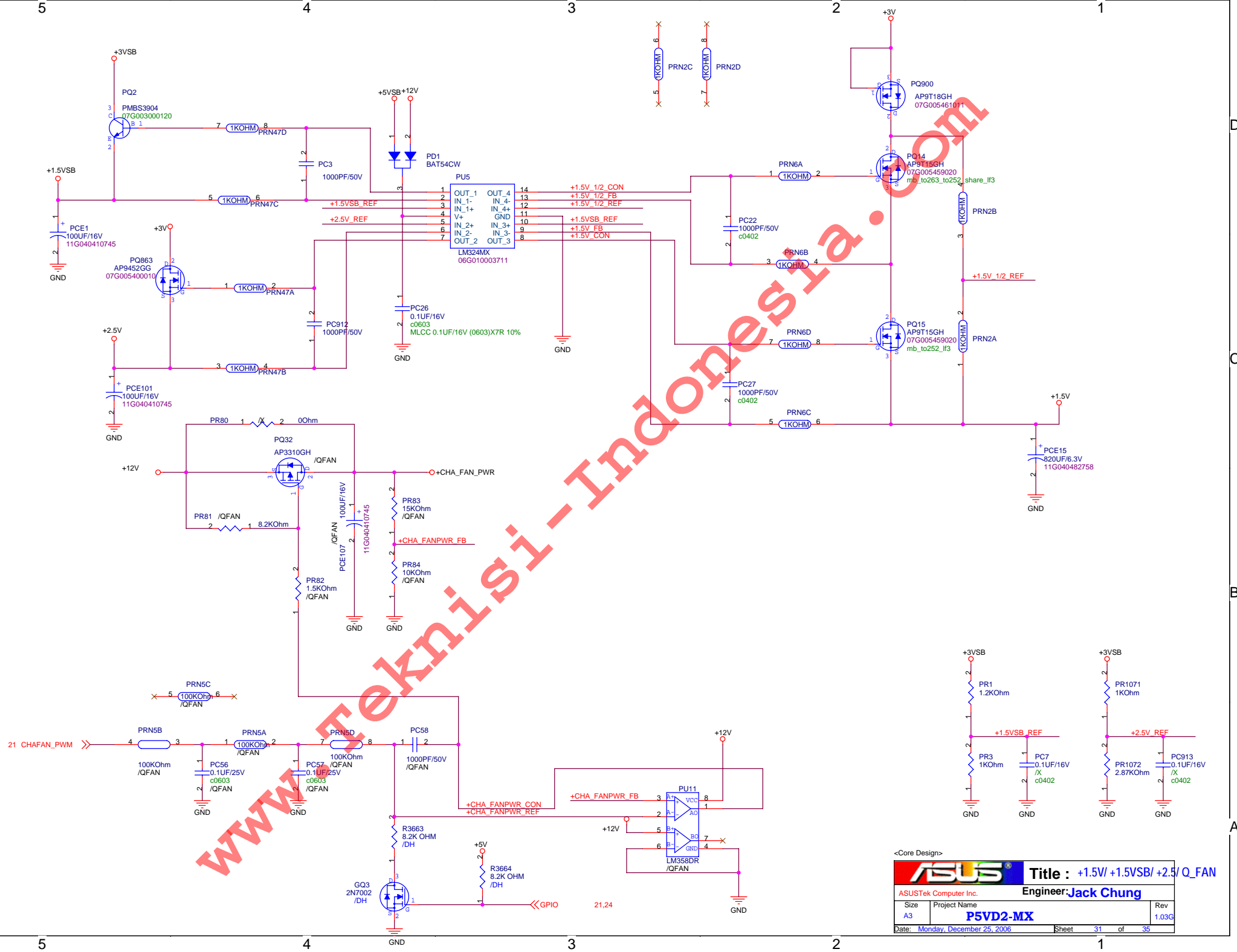


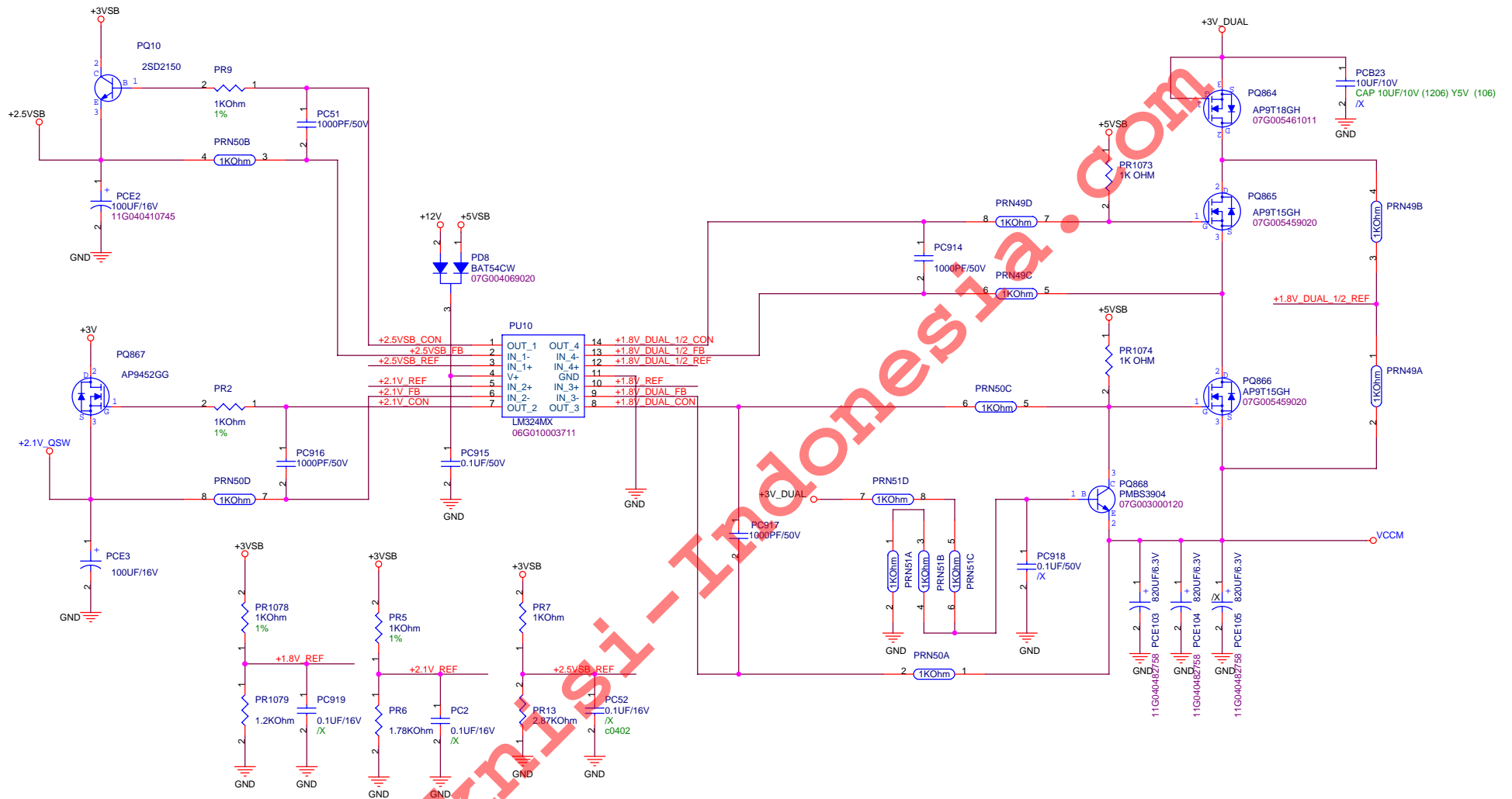
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ASUSTek Computer Inc.		Engineer: Jack Chung	
Size	Project Name		Rev
A3	P5VD2-MX		1.03G
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<Core Design>

ASUS		Title : VCCM/ +2.1V_QSW/ +2.5VSB	
ASUSTek Computer Inc.		Engineer: Jack Chung	
Size A3	Project Name P5VD2-MX		Rev 1.03G
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